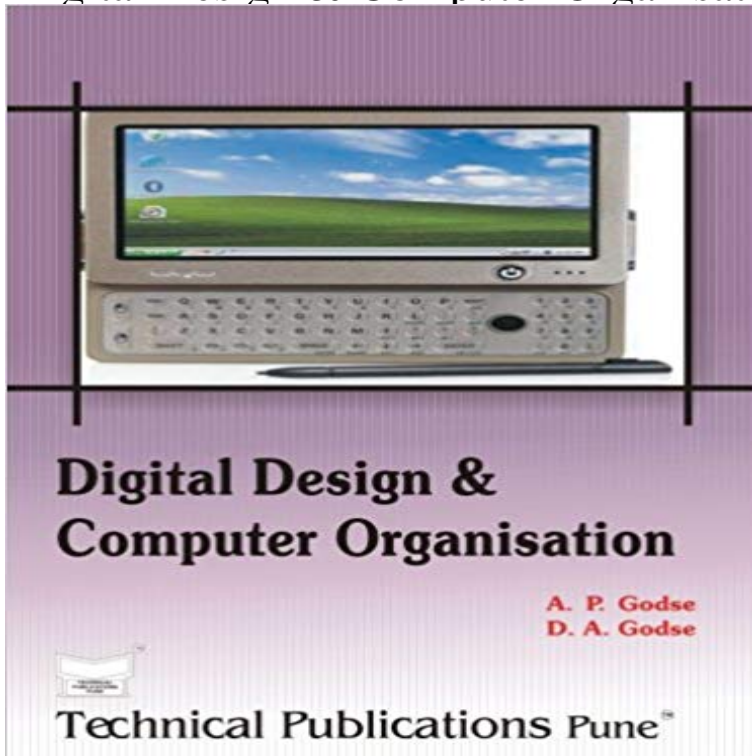


# Digital Design & Computer Organisation



Sequential Circuit Design : Synchronous and asynchronous FSM design, Basic design steps, State encoding techniques, VHDL coding of state machines, Analysis of sequential circuits, Algorithmic State Machine (ASM) charts. HDL : Introduction to HDL, VHDL, Library, Entity, Architecture, Modeling styles, Signals and variables, Sequential and concurrent statements, Synthesis and simulation concepts, Implementation of logic function. ALU Design : Addition and subtraction of signed numbers, Design of fast adders, Multiplication of positive numbers, Signed operand multiplication - Booths algorithm, Fast multiplication - Bit pair recoding of multipliers. Carry save addition of summands. Integer division, Floating point numbers and operation, IEEE standards for floating point numbers, Arithmetic operations on floating point numbers, Guard bits and truncation, Implementing floating point operation. CPU Design : Memory operation, Instruction and instruction sequencing, Addressing modes, Assembly language, Basic input/output operations, Stacks and queues, Subroutines, Execution of a complete instruction, Multiple bus organization. Input/Output Organization : Accessing I/O devices, Interrupts, Direct memory access, Buses, Interface circuits. Memory Organization : Semiconductor RAM memory, Read only memory, Speed, Size and cost, Cache memories, Performance considerations, Virtual memories, Memory management requirements, Secondary storage.

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